

# Multilayer Ceramic Chip Capacitors

## C4532CH2E104K320KN



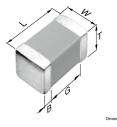






#### TDK item description C4532CH2E104KT\*\*\*\*

Applications	Commercial Grade	
Feature	Mid Mid Voltage (100 to 630V)	
Series	C4532 [EIA 1812]	
Status	Production (Not Recommended for New Design)	



	Size	
Length(L)	4.50mm ±0.40mm	
Width(W)	3.20mm ±0.40mm	
Thickness(T)	3.20mm ±0.30mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)		
Recommended Land Pattern (PA)	3.10mm to 3.70mm	
Recommended Land Pattern (PB)	1.20mm to 1.40mm	
Recommended Land Pattern (PC)	2.40mm to 3.20mm	

Electrical Characteristics		
Capacitance	100nF ±10%	
Rated Voltage	250VDC	
Temperature Characteristic	CH(0±60ppm/°C)	
Q (Min.)	1000	
Insulation Resistance (Min.)	5000ΜΩ	

Other		
Soldering Method	Reflow	
AEC-Q200	No	
Packing	Blister (Plastic)Taping [180mm Reel]	
Package Quantity	500pcs	

<sup>!</sup> Images are for reference only and show exemplary products. ! This PDF document was created based on the data listed on the TDK Corporation website.

<sup>!</sup> All specifications are subject to change without notice.

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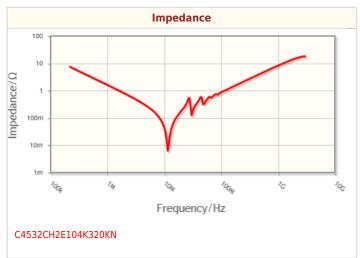


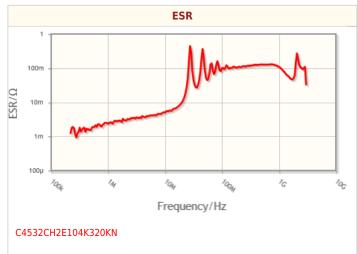


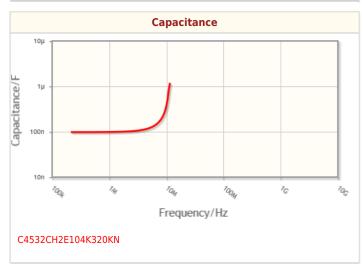


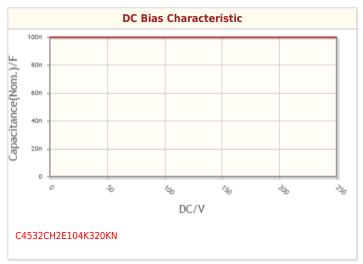


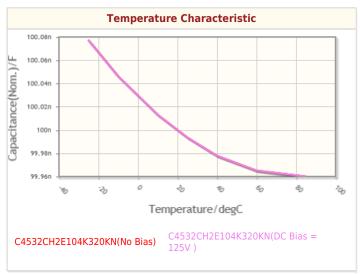
# Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)











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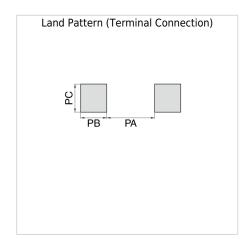








# **Associated Images**



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